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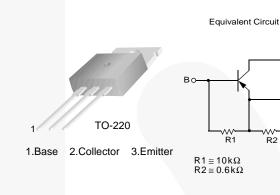
## November 2014

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# TIP115 / TIP117 PNP Epitaxial Silicon Darlington Transistor

## Features

- Monolithic Construction with Built-in Base-Emitter Shunt Resistors
- High DC Current Gain:
- $h_{FE} = 1000 @ V_{CE} = -4 V, I_{C} = -1 A (Minimum)$
- Low Collector-Emitter Saturation Voltage
- Industrial Use
- Complementary to TIP110 / TIP111 / TIP112



## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
TIP115	TIP115	TO-220 3L (Single Gauge)	Bulk
TIP117TU	TIP117	TO-220 3L (Single Gauge)	Rail

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_c = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Value	Unit	
V <sub>CBO</sub> Co	Collector-Base Voltage	TIP115	-60	- V	
		TIP117	-100		
V <sub>CEO</sub>	Collector-Emitter Voltage	TIP115	-60	V	
		TIP117	-100	V	
V <sub>EBO</sub>	Emitter-Base Voltage		-5	V	
Ι <sub>C</sub>	Collector Current (DC)		-2	A	
I <sub>CP</sub>	Collector Current (Pulse)		-4	А	
I <sub>B</sub>	Base Current (DC)		-50	mA	
Τ <sub>J</sub>	Junction Temperature		150	°C	
T <sub>STG</sub>	Storage Temperature Range		-65 to 150	°C	

## **Thermal Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit	
Po	Collector Dissipation ( $T_A = 25^{\circ}C$ )	2	W	
L_C	Collector Dissipation ( $T_C = 25^{\circ}C$ )	50	vv	

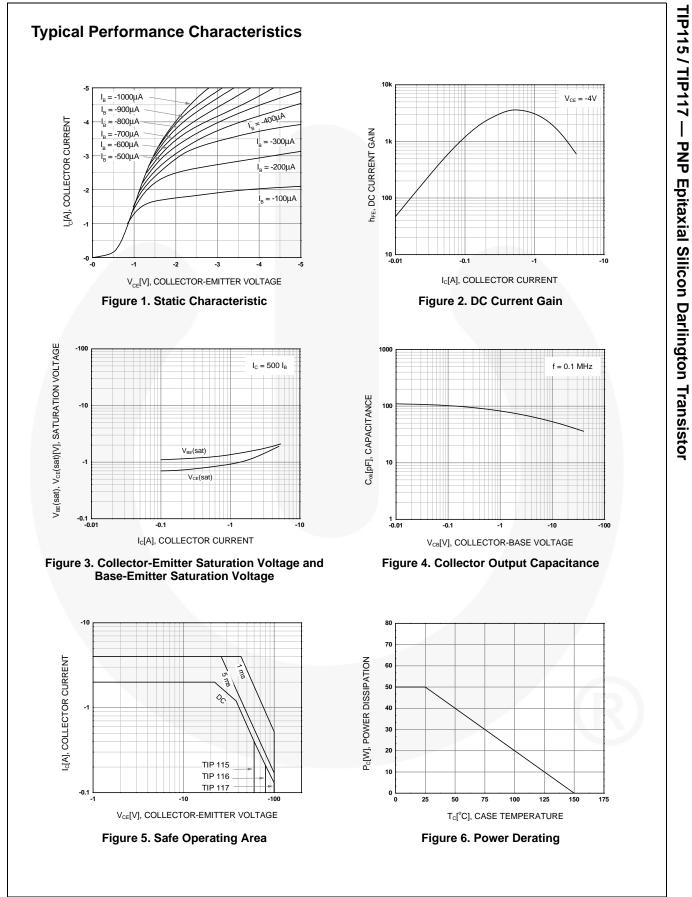
## **Electrical Characteristics**<sup>(1)</sup>

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

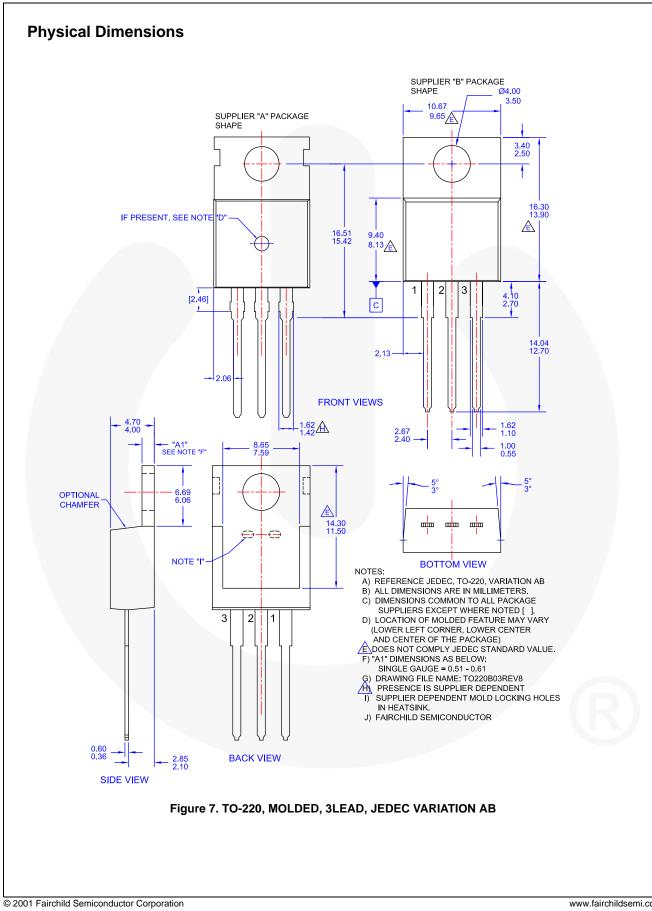
Symbol	Parameter		Conditions	Min.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage	TIP115	- I <sub>C</sub> = -30 mA, I <sub>B</sub> = 0	-60		V
		TIP117		-100		
I <sub>CEO</sub>	Collector Cut-Off Current	TIP115	$V_{CE} = -30 \text{ V}, \text{ I}_{B} = 0$		-2	mA
		TIP117	$V_{CE} = -50 \text{ V}, \text{ I}_{B} = 0$		-2	
I <sub>CBO</sub>	Collector Cut-Off Current	TIP115	$V_{CB} = -60 \text{ V}, \text{ I}_{E} = 0$		-1	mA
		TIP117	V <sub>CB</sub> = -100 V, I <sub>E</sub> = 0		-1	
I <sub>EBO</sub>	Emitter Cut-Off Current		$V_{EB} = -5 V, I_{C} = 0$		-2	mA
h <sub>FE</sub>	DC Current Gain		$V_{CE} = -4 V, I_{C} = -1 A$	1000		
			$V_{CE} = -4 \text{ V}, \text{ I}_{C} = -2 \text{ A}$	500		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage		I <sub>C</sub> = -2 A, I <sub>B</sub> = -8 mA		-2.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage		$V_{CE} = -4 \text{ V}, \text{ I}_{C} = -2 \text{ A}$		-2.8	V
C <sub>ob</sub>	Output Capacitance		$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0,$ f = 0.1 MHz		200	pF

### Note:

1. Pulse test:  $pw \le 300 \ \mu s$ , duty cycle  $\le 2\%$ .



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**TIP115 / TIP117** 

- PNP Epitaxial Silicon Darlington Transistor

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