N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω) Max.	I_D (A)	Q_g (Typ.)
100	0.076 at V_{GS} = 10 V	9[d]	8.5
	0.096 at V_{GS} = 6 V	9[d]	

FEATURES

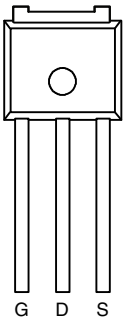
- **Halogen-free According to IEC 61249-2-21 Definition**
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

RoHS COMPLIANT
HALOGEN FREE

APPLICATIONS

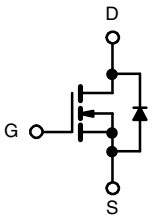
- DC/DC Converters
- Motor Control

TO-251



Drain connected to DRAIN-TAB

Top View



N-Channel MOSFET

Ordering Information
SUU09N10-76P-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	9[d]	A
	T_C = 70 °C		9[d]	
Pulsed Drain Current (t = 300 µs)		I_{DM}	20	
Avalanche Current		I_{AS}	18	
Single Avalanche Energy[a]	L = 0.1 mH	E_{AS}	16.2	mJ
Maximum Power Dissipation[a]	T_C = 25 °C	P_D	32.1[b]	W
	T_A = 25 °C[c]		2.5	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS			
Parameter	**Symbol**	**Limit**	**Unit**
Junction-to-Ambient (PCB Mount)[c]	R_{thJA}	50	°C/W
Junction-to-Case (Drain)	R_{thJC}	3.9	

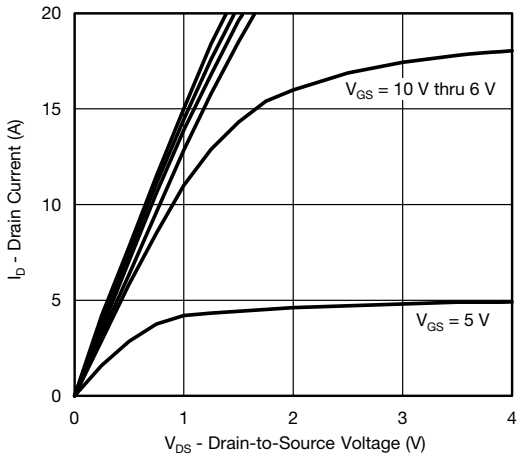
Notes:
a. Duty cycle ≤ 1 %.
b. See SOA curve for voltage derating.
c. When mounted on 1" square PCB (FR-4 material).
d. Package limited

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	2.5		4	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100 V, V_{GS} = 0 V			1	µA
		V_{DS} = 100 V, V_{GS} = 0 V, T_J = 125 °C			50	
		V_{DS} = 100 V, V_{GS} = 0 V, T_J = 150 °C			250	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 10 V, V_{GS} = 10 V	15			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 6.1 A		0.063	0.076	Ω
		V_{GS} = 6 V, I_D = 5.4 A		0.080	0.096	
Forward Transconductance[a]	g_{fs}	V_{DS} = - 20 V, I_D = 6.1 A		13		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 50 V, f = 1 MHz		505		pF
Output Capacitance	C_{oss}			71		
Reverse Transfer Capacitance	C_{rss}			35		
Total Gate Charge[c]	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 6.1 A		12.7	19.1	nC
		V_{DS} = 50 V, V_{GS} = 4.5 V, I_D = 6.1 A		8.5	12.8	
Gate-Source Charge[c]	Q_{gs}			3.6		
Gate-Drain Charge[c]	Q_{gd}			4		
Gate Resistance	R_g	f = 1 MHz	0.2	0.9	1.8	Ω
Turn-On Delay Time[c]	$t_{d(on)}$	V_{DD} = 50 V, R_L = 10.2 Ω I_D ≅ 4.9 A, V_{GEN} = 10 V, R_g = 1 Ω		7	14	ns
Rise Time[c]	t_r			11	20	
Turn-Off Delay Time[c]	$t_{d(off)}$			11	20	
Fall Time[c]	t_f			6	12	
Drain-Source Body Diode Ratings and Characteristics T_C = 25 °C[b]						
Continuous Current	I_S				9	A
Pulsed Current	I_{SM}				20	
Forward Voltage[a]	V_{SD}	I_F = 4.9 A, V_{GS} = 0 V		0.82	1.5	V
Reverse Recovery Time	t_{rr}	I_F = 4.9 A, dI/dt = 100 A/µs		36	53	ns
Peak Reverse Recovery Current	$I_{RM(REC)}$			2.7	4.1	A
Reverse Recovery Charge	Q_{rr}			46	69	nC

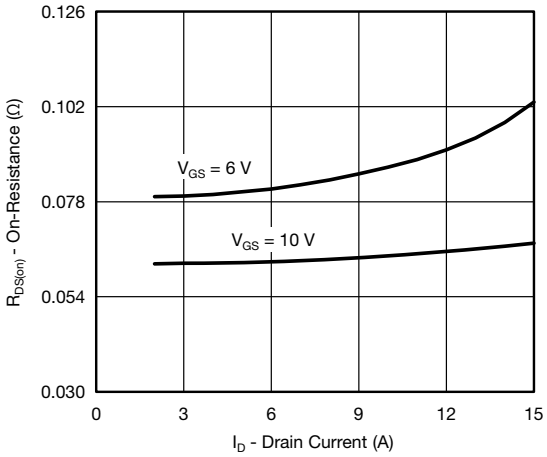
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

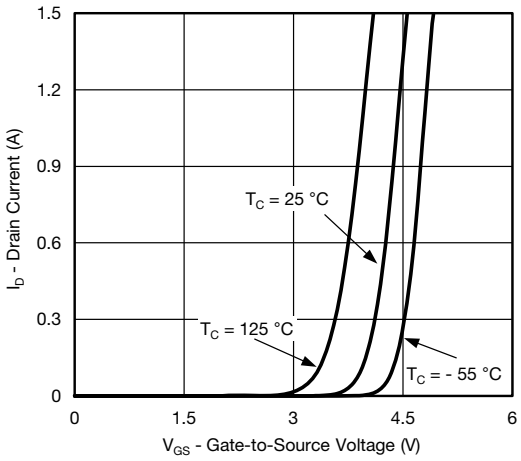
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



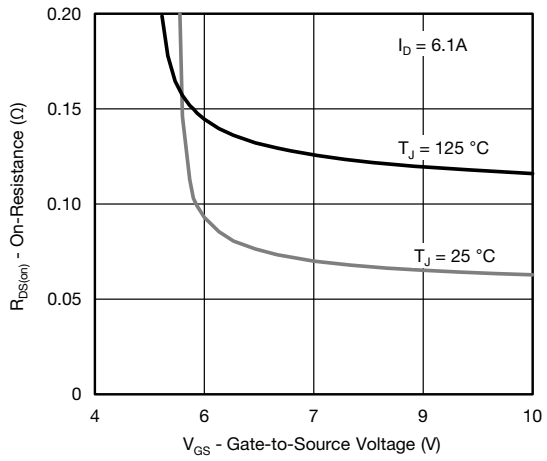
Output Characteristics



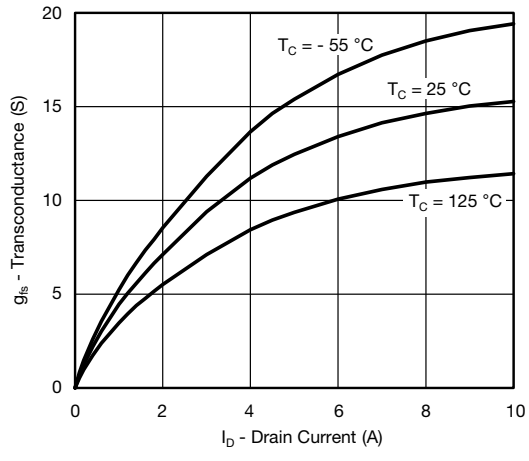
On-Resistance vs. Drain Current



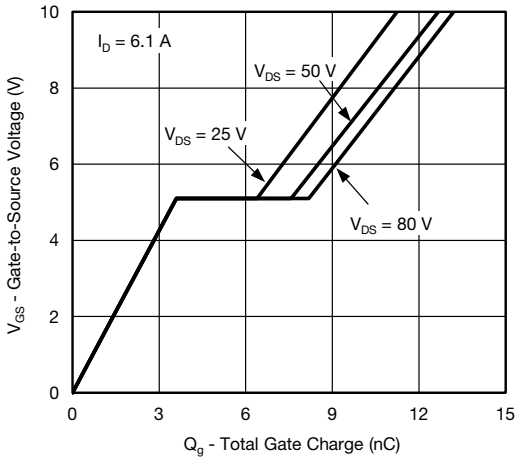
Transfer Characteristics



On-Resistance vs. Gate-to-Source Voltage

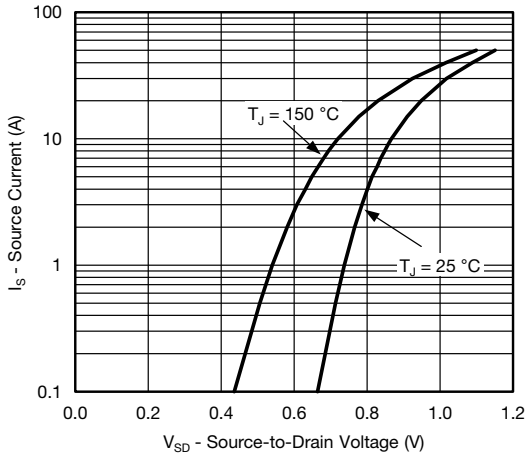


Transconductance

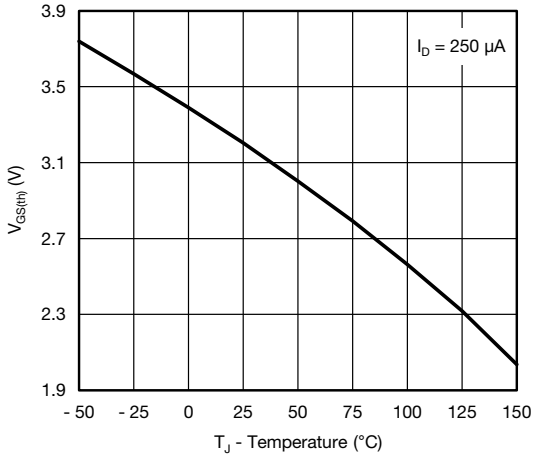


Gate Charge

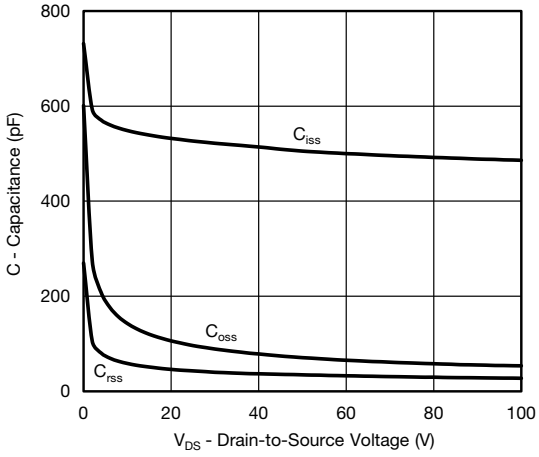
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



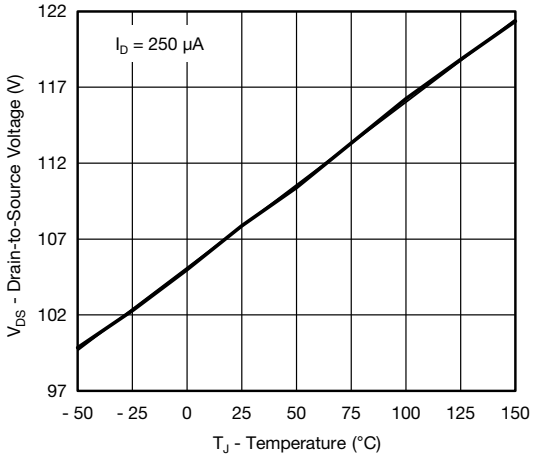
Source-Drain Diode Forward Voltage



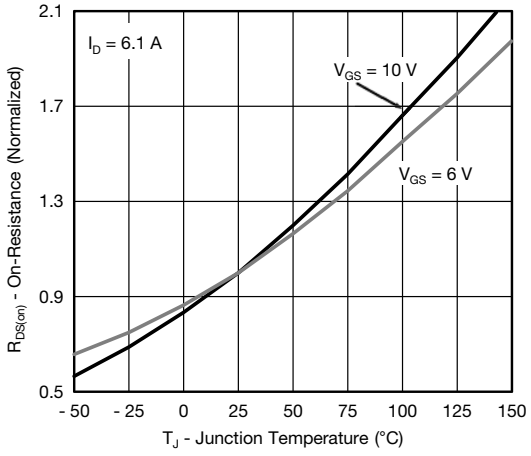
Threshold Voltage



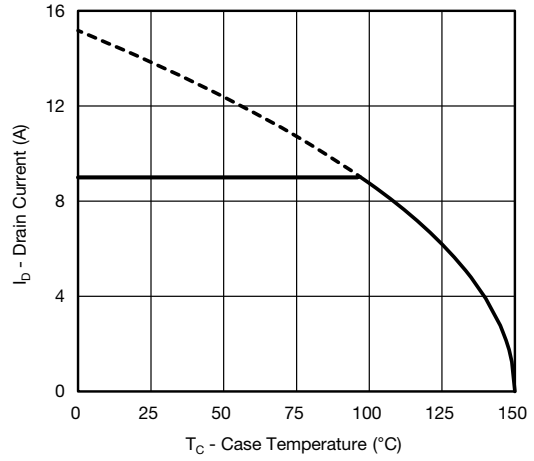
Capacitance



Drain Source Breakdown vs. Junction Temperature

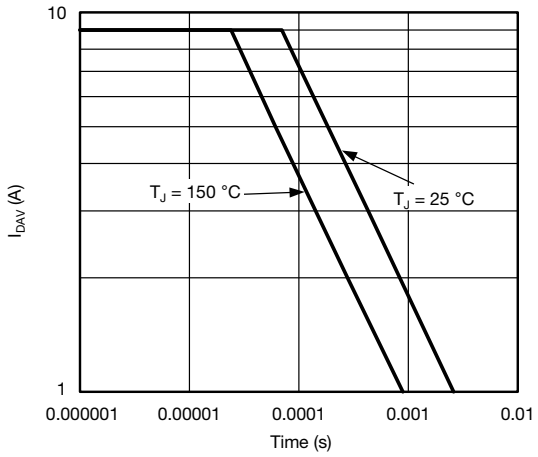


On-Resistance vs. Junction Temperature

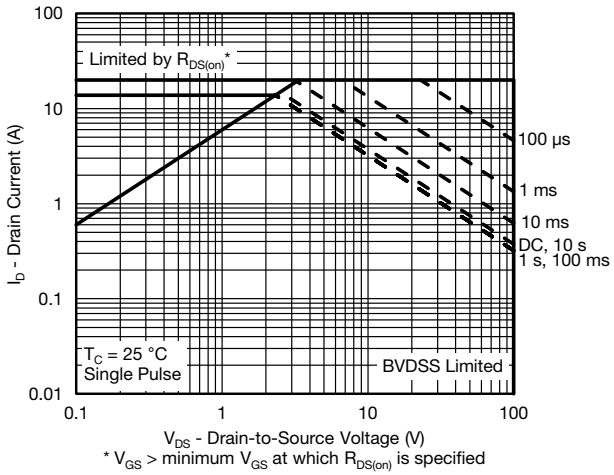


Current Derating

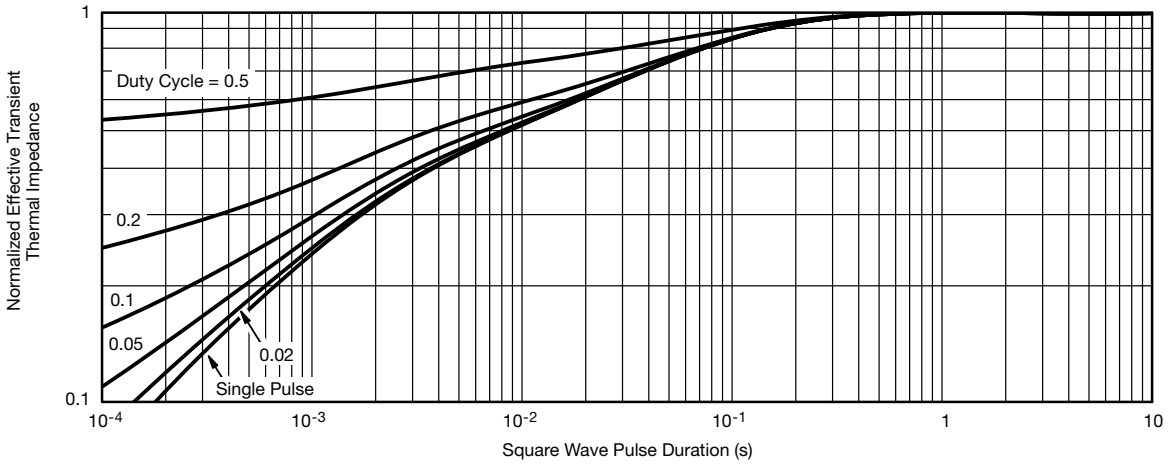
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Single Pulse Avalanche Current Capability vs. Time



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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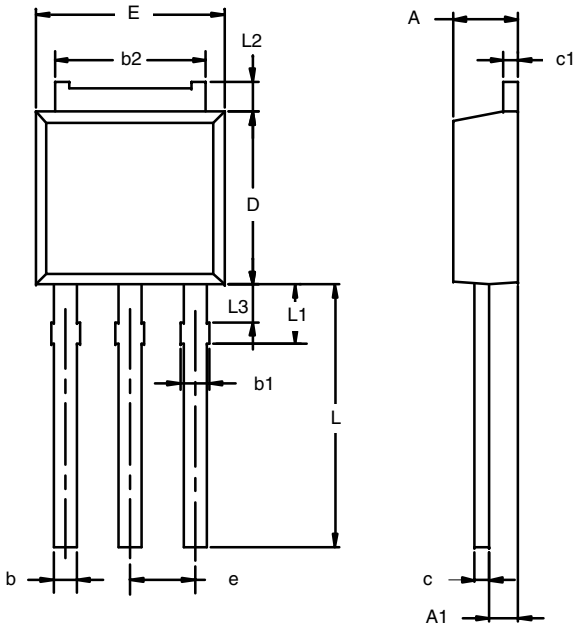
TO-251AA (IPAK)



	MILLIMETERS		INCHES	
DIM.	MIN.	MAX.	MIN.	MAX.
A	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
C	0.46	0.58	0.018	0.023
C1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
E	6.48	6.73	0.255	0.265
e	2.28 BSC		0.090 BSC	
L	8.89	9.53	0.350	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060
ECN: T13-0362-Rev. F, 03-Jun-13 DWG: 5346				

Note

- Dimension L3 is for reference only.

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