

LM747 **Dual Operational Amplifier**

General Description

The LM747 is a general purpose dual operational amplifier. The two amplifiers share a common bias network and power supply leads. Otherwise, their operation is completely independent.

Additional features of the LM747 are: no latch-up when input common mode range is exceeded, freedom from oscillations, and package flexibility.

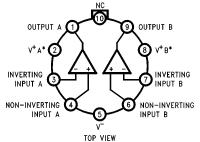
The LM747C/LM747E is identical to the LM747/LM747A except that the LM747C/LM747E has its specifications guaranteed over the temperature range from 0°C to +70°C instead of -55°C to +125°C.

Features

- No frequency compensation required
- Short-circuit protection
- Wide common-mode and differential voltage ranges
- Low power consumption
- No latch-up
- Balanced offset null

Connection Diagrams

Metal Can Package

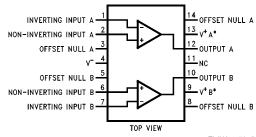


TI /H/11479-4

Order Number LM747H See NS Package Number H10C

 $^*\mathsf{V}^+\mathsf{A}$ and $\mathsf{V}^+\mathsf{B}$ are internally connected.

Dual-In-Line Package



Order Number LM747CN or LM747EN See NS Package Number N14A

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage

 Input Voltage (Note 2)
Output Short-Circuit Duration
Operating Temperature Range

Operating Temperature Range LM747/LM747A LM747C/LM747E Storage Temperature Range

-55°C to +125°C 0°C to +70°C -65°C to +150°C

Lead Temperature (Soldering, 10 sec.)

300°C

 $\pm\,15V$

Indefinite

Electrical Characteristics (Note 3)

Parameter	Conditions	LM747A/LM747E			LM747			LM747C			
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Units
Input Offset Voltage	$\begin{aligned} &T_{A} = 25^{\circ}C \\ &R_{S} \leq 10 \; k\Omega \\ &R_{S} \leq 50\Omega \end{aligned}$		0.8	3.0		1.0	5.0		2.0	6.0	mV
	$\begin{aligned} &R_{S} \leq 50\Omega \\ &R_{S} \leq 10 \; k\Omega \end{aligned}$			4.0			6.0			7.5	mV
Average Input Offset Voltage Drift				15							μV/°C
Input Offset Voltage Adjustment Range	$T_A = 25^{\circ}C, V_S = \pm 20V$	±10				±15			±15		mV
Input Offset Current	$T_A = 25^{\circ}C$		3.0	30		20	200		20	200	nA
				70		85	500			300	
Average Input Offset Current Drift				0.5							nA/°C
Input Bias Current	$T_A = 25^{\circ}C$ $T_{AMIN} \le T_A \le T_{AMAX}$		30	80 0.210		80	500 1.5		80	500 0.8	nA μA
Input Resistance	$T_A = 25^{\circ}C, V_S = \pm 20V$	1.0	6.0		0.3	2.0		0.3	2.0		ΜΩ
	$V_S = \pm 20V$	0.5									
Input Voltage Range	$T_A = 25^{\circ}C$	±12	± 13		±12	± 13		±12	±13		V
Large Signal Voltage Gain	$T_A = 25^{\circ}\text{C}, R_L \ge 2 \text{ k}\Omega$ $V_S = \pm 20\text{V}, V_O = \pm 15\text{V}$	50									V/mV
	$V_S = \pm 15V, V_O = \pm 10V$ $R_L \ge 2 k\Omega$				50	200		20	200		V/mV
	$V_S = \pm 20V, V_O = \pm 15V$	32									V/mV
	$V_S = \pm 15V, V_O = \pm 10V$				25			15			V/mV
	$V_S = \pm 5V, V_O = \pm 2V$	10									V/mV
Output Voltage Swing	$\begin{split} V_S &= \pm 20V \\ R_L &\geq 10 \ k\Omega \\ R_L &\geq 2 \ k\Omega \end{split}$	±16 ±15									٧
	$\begin{split} V_S &= \pm 15 V \\ R_L &\geq 10 \ k \Omega \\ R_L &\geq 2 \ k \Omega \end{split}$				±12 ±10	± 14 ± 13		±12 ±10	± 14 ± 13		٧
Output Short Circuit Current	$T_A = 25^{\circ}C$	10 10	25	35 40		25			25		mA
Common-Mode Rejection Ratio	$R_{S} \leq 10~k\Omega, V_{CM} =~\pm12V$				70	90		70	90		- dB
	$R_S \le 50 \text{ k}\Omega, V_{CM} = \pm 12V$	80	95								

Electrical Characteristics (Note 3) (Continued)

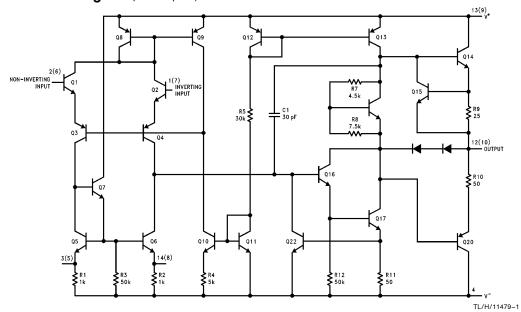
Parameter	Conditions	LM747A/LM747E			LM747			LM747C			
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Units
Supply Voltage Rejection Ratio	$V_S = \pm 20 V$ to $V_S = \pm 5 V$ $R_S \le 50 \Omega$ $R_S \le 10 \text{ k}\Omega$	86	96		77	96		77	96		dB
Transient Response Rise Time Overshoot	T _A = 25°C, Unity Gain		0.25 6.0	0.8 20		0.3 5			0.3 5		μs %
Bandwidth (Note 4)	$T_A = 25^{\circ}C$	0.437	1.5								MHz
Slew Rate	T _A = 25°C, Unity Gain	0.3	0.7			0.5			0.5		V/µs
Supply Current/Amp	$T_A = 25^{\circ}C$			2.5		1.7	2.8		1.7	2.8	mA
Power Consumption/Amp	$T_A = 25^{\circ}C$ $V_S = \pm 20V$ $V_S = \pm 15V$		80	150		50	85		50	85	mW
LM747A	$V_S = \pm 20V$ $T_A = T_{AMIN}$ $T_A = T_{AMAX}$			165 135							mW
LM747E	$V_S = \pm 20V$ $T_A = T_{AMIN}$ $T_A = T_{AMAX}$			150 150 150							mW
LM747	$V_S = \pm 15V$ $T_A = T_{AMIN}$ $T_A = T_{AMAX}$					60 45	100 75				mW

Note 1: The maximum junction temperature of the LM747C/LM747E is 100°C. For operating at elevated temperatures, devies in the TO-5 package must be derated based on a thermal resistance of 150°C/W, junction to ambient, or 45°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

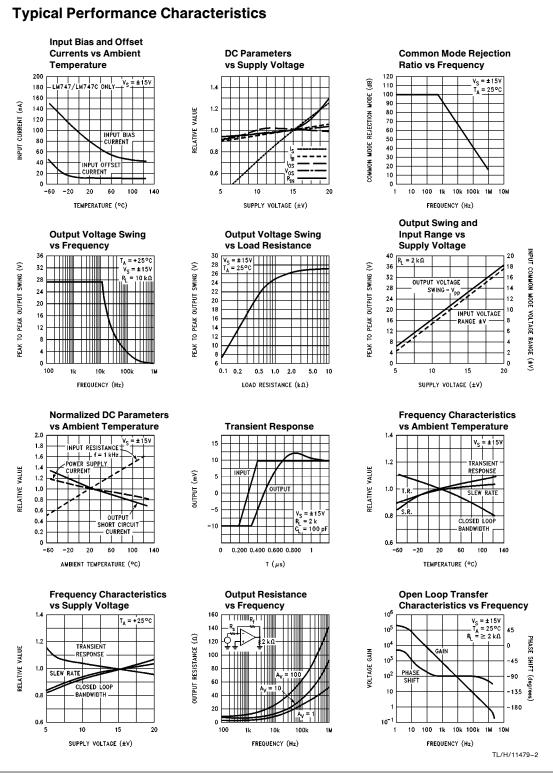
Note 2: For supply voltages less than $\pm 15V$, the absolute maximum input voltage is equal to the supply voltage.

Note 3: These specifications apply for $\pm 5\text{V} \le \text{V}_S \le \pm 20\text{V}$ and $-55^\circ\text{C} \le \text{T}_A \le 125^\circ\text{C}$ for the LM747A and $0^\circ\text{C} \le \text{T}_A \le 70^\circ\text{C}$ for the LM747E unless otherwise specified. The LM747 and LM747C are specified for $\text{V}_S = \pm 15\text{V}$ and $-55^\circ\text{C} \le \text{T}_A \le 125^\circ\text{C}$ and $0^\circ\text{C} \le \text{T}_A \le 70^\circ\text{C}$, respectively, unless otherwise specified. Note 4: Calculated value from: 0.35/Rise Time (μ s).

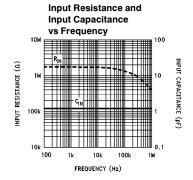
Schematic Diagram (Each Amplifier)

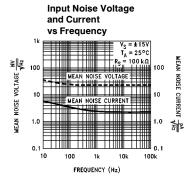


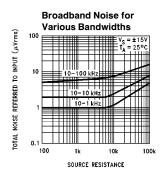
Note: Numbers in parentheses are pin numbers for amplifier B. DIP only.

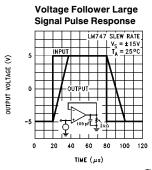


Typical Performance Characteristics (Continued)

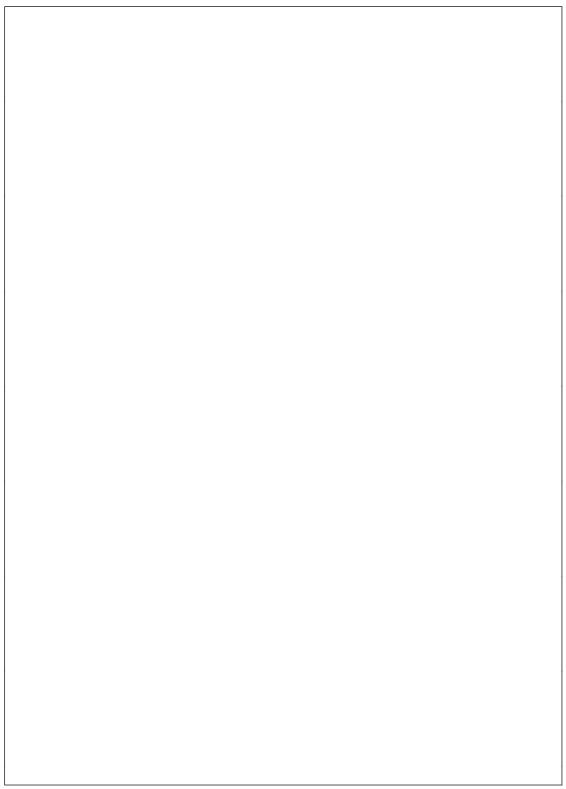




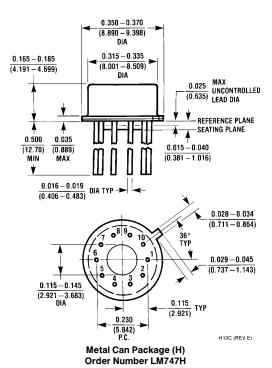




TL/H/11479-3



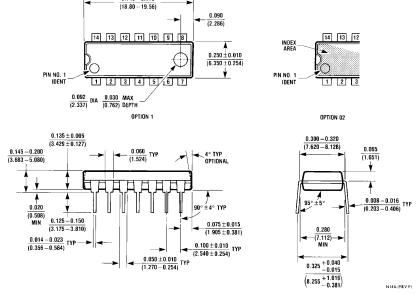




NS Package Number H10C

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Physical Dimensions inches (millimeters) (Continued)



Dual-In-Line Package (N) Order Number LM747CN or LM747EN NS Package Number N14A

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National Semiconductor

National Semiconducto Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018

National Semiconductor Europe

Fax: (+49) 0-180-530 85 86 Fax: (+49) U-18U-35U oo oo Email: onjwege tevm2.nsc.com Deutsch Tel: (+49) 0-180-530 85 85 English Tei: (+49) 0-180-532 78 32 Français Tel: (+49) 0-180-532 93 58 Italiano Tel: (+49) 0-180-534 16 80 **National Semiconductor** Hong Kong Ltd.
13th Floor, Straight Block,
Ocean Centre, 5 Canton Rd.

Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960

National Semiconductor

Japan Ltd.
Tel: 81-043-299-2309
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