

ATTP1

Over Temperature Protection Controller

Datasheet

Release Date: April. 2002

Revision: 1.0

Revision History

Version	Date	Changes from Last Version

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1. General Description

ATTP1 is system over temperature protection device and is equipped one Thermo Diode and one Thermistor sensing inputs. When the temperature reaches the preset trigger value (Over Temperature Protection, OTP, event occurred), PSON_OUT is pulled up to turn the system power off, and a siren signal is sent out. PSON_OUT will keep logic high until AC/5VSB power loss, or PWRBNT pressed. The trigger temperature/value of Thermo Diode and thermistor can be individually set by the corresponding external resistor.

2. Features

- Two sets of temperature sensing mechanism
 - One Thermo Diode input for embedded thermo diode in CPU
 - One Thermistor input supporting legacy CPU
- Programmable trigger temperature by external resistor
- OTP event latched until power loss, or pressing Power Button
- Real time monitoring and reacting (Less than 1 μ second)
- Siren alarming users during OTP event occurrence
- Accuracy: $\pm 5^{\circ}\text{C}$
- Package: SOP 8-Pin

3. Pin Configuration

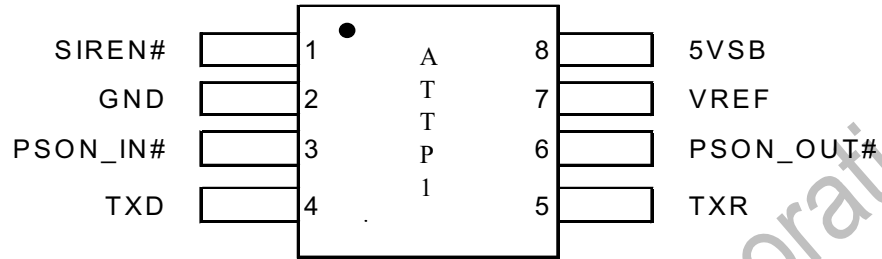
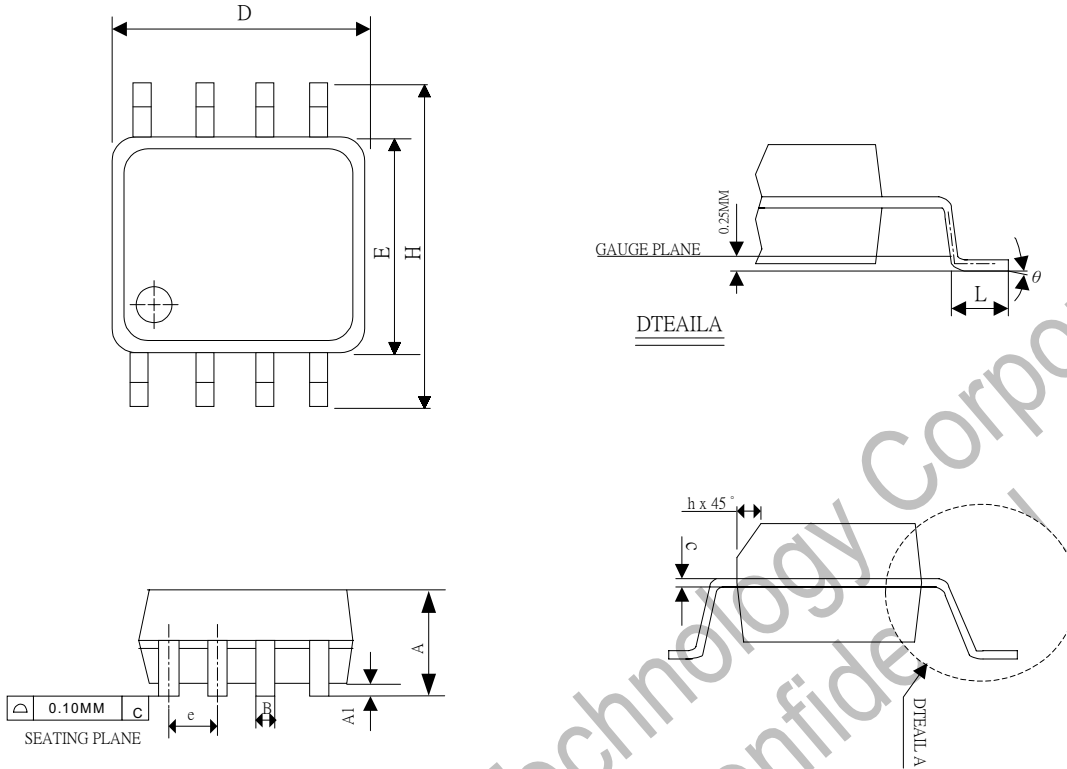


Figure 1. ATTP1 Pin Diagram (Top View)

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4. Package Information SOP8pin Outline Dimension



Symbol	Dimension in mm		Dimension in inch	
	Min	Max	Min	Max
A	1.35	1.75	0.0532	0.0688
A1	0.10	0.25	0.0040	0.0098
B	0.33	0.51	0.013	0.020
C	0.19	0.25	0.0075	0.0098
e	1.27BSC		0.050BSC	
D	4.80	5.00	0.1890	0.1968
H	5.80	6.20	0.2284	0.2440
E	3.80	4.00	0.1497	0.1574
L	0.40	1.27	0.016	0.050
h	0.25	0.50	0.0099	0.0196
θ	0°	8°	0°	8°
JEDEC	MS-012 (AA)			

*NOTES: DIMENSION "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.15 MM (0.006 INCH) PER SIDE.

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